
COMBINED DECLARATION AND POWER OF ATTORNEY

**(ORIGINAL, DESIGN, NATIONAL STAGE OF PCT, SUPPLEMENTAL, DIVISIONAL,
CONTINUATION, OR C-I-P)**

As a below named inventor, I hereby declare that:

TYPE OF DECLARATION

This declaration is for a national stage of PCT application.

INVENTORSHIP IDENTIFICATION

My residence, post office address and citizenship are as stated below, next to my name. I believe that I am an original, first and joint inventor of the subject matter that is claimed, and for which a patent is sought on the invention entitled:

TITLE OF INVENTION

Printing of Electronic Circuits & Components

SPECIFICATION IDENTIFICATION

The specification was described and claimed in PCT International Application No. PCT/US98/25088 filed on November 24, 1998 and was amended under PCT Article 19 on March 2, 2000.

ACKNOWLEDGMENT OF REVIEW OF PAPERS AND DUTY OF CANDOR

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in 37, Code of Federal Regulations, Section 1.56.

CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S)

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of provisional application number 60/097,706, filed August 21, 1998.

POWER OF ATTORNEY

I hereby appoint the following practitioner(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

APPOINTED PRACTITIONER(S)

Robert D. Fish

David J. Zoetewey

Sandra Poteat Thompson

Martin Fessenmaier

Davis Edward

REGISTRATION NUMBER(S)

33,880

45,258

46,264

46,697

35,112

SEND CORRESPONDENCE TO

Sandra Poteat Thompson
1440 N. Harbor Blvd.
Suite 706
Fullerton, CA 92835
Customer Number 24392

DIRECT TELEPHONE CALLS TO:

Sandra Poteat Thompson
714-449-2337

DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

SIGNATURE(S)

1-20
Subhash Narang

Inventor's signature

Date

3/21/01

Country of Citizenship US

Residence Palo Alto, CA

Post Office Address SRI International, 333 Ravenswood Avenue, Menlo Park, CA 94025

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